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EE 121: Digital Design Laboratory - Stanford -

Digital Design Laboratory digital displays The EE 121 lab has seven stations, Digital Design Principles & Practices

<http://web.stanford.edu/class/ee121/>

Cadence Design Systems Software | The Cooper -

This page contains information about the Cadence Cadence Design Systems Software. ICE Lab; Bandgap reference circuits. MOS digital circuit design and layout,

<http://www.cooper.edu/engineering/facilities/electrical-engineering/cadence-design-systems-software>

Spring 2014 - ESE570 Digital VLSI Circuits -

Design simulated experiments using Cadence to verify the integrity "VLSI Design Techniques for Analog and Digital Circuits or_ lab results

<http://www.seas.upenn.edu/%7Eese570/>

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Kendall Hunt -

Digital Design Laboratory Manual. Author(s): Thomas Collins,

http://www.kendallhunt.com/digital_design/

VTVT - VLSI Design: Cadence Tutorial - Virginia -

Cadence. Circuit Design. Analog; Digital; In this tutorial we are using the Cadence's SOC Encounter version 5.2 [Cadence]/soc_enc>encounter. 3. DESIGN

http://www.vtvt.ece.vt.edu/vlsidesign/tutorialCadence_socEncounter.php

VLSI Lab Manual VTU EC - Scribd -

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CS/EE 5710/6710 Digital VLSI - University of Utah -

We'll also use Digital VLSI Chip Design with Cadence and Synopsys CAD Tools by Erik Brunvand as a lab manual. what is available in our University of Utah

<http://www.eng.utah.edu/~cs6710/>

Cadence - ECE Wiki -

Cadence design tools are used they are used by several research groups in the design of chips integrating analog, digital, The lab manuals which describe

<http://wiki.ece.cmu.edu/index.php/Cadence>

EEE 120 Digital Design Fundamentals (3) [F, S, SS] -

Laboratory Manual: Digital Design for the Laboratory: Hardware and Simulation (using LogicWorks(4) Ver. 1, EEE 120 Digital Design Fundamentals (3)

http://www.engineering.asu.edu/sites/default/files/shared/syllabi/EEE120_FA09_%20DDF.doc

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